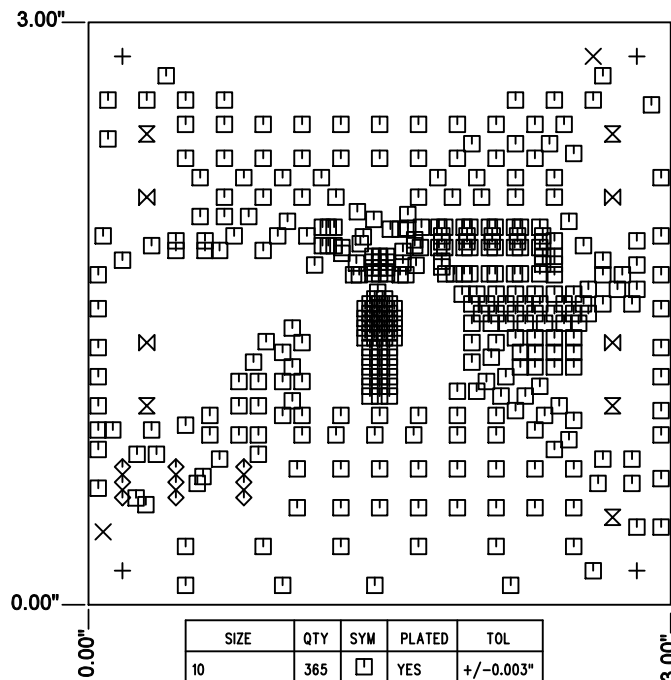
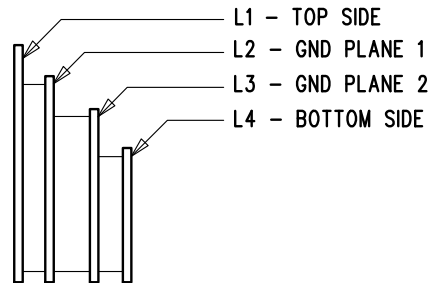


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	3	PRODUCTION	JB	10/5/16

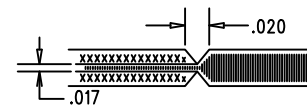
LAYER STRUCTURE


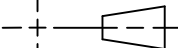


SIZE	QTY	SYM	PLATED	TOL
10	365	□	YES	+/-0.003"
35	9	◇	YES	+/-0.003
70	2	×	NO	+/-0.003"
94	5	⊗	YES	+/-0.003"
190	4	+	YES	+/-0.003"
205	4	⊠	YES	+/-0.003

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600, CLASS 2.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.031" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON ALL LAYERS
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- DO NOT ALTER SOLDER MASK MAINTAIN .0018" OVERSIZE ON SMT PADS. A .005" WEBBING IS REQUIRED BETWEEN SMD PADS.
- SCORING FOR PANELIZED PCB: "PRODUCTION FAB ONLY"



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ± 1 0.XX" = ± 0.01 " 0.XXX" = ± 0.005 " INTERPRET DIM AND TOL PER ASME Y14.5M-1994	APPROVALS		 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.Linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY		
	PCB DES.	NC			
	APP ENG.	JB			
THIRD ANGLE PROJECTION			TITLE: FABRICATION DRAWING: 18V, 5A SYNCHRONOUS BUCK-BOOST DC/DC CONVERTER		
					
DO NOT SCALE DRAWING	SCALE: NONE		SIZE N/A	IC NO. LTC3119 DEMO CIRCUIT 2129A	REV. 3
			SHT 1 of 1		